



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Group Art Unit: Unassigned
Examiner: Unassigned

In re PATENT APPLICATION of

Applicant(s) : Toru MORI

Appln. No. : 10/748,259

Filed : December 31, 2003

For : MANUFACTURING METHOD FOR
SOI SEMICONDUCTOR DEVICE, AND
SOI SEMICONDUCTOR DEVICE

Atty. Dkt. : FUJI 139

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)
)
) **SUBMISSION OF**
) **FORMAL DRAWINGS**
)
)
)
)
)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits seven replacement drawings, Figures 1 to 14. Figures 9 and 10 each have been revised to correct a reference numeral by changing "33" to "32".

Respectfully submitted,

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March 11, 2004

Date

SMR/pjl

FEE ENCLOSED:\$
Please charge any further
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No. 18-0002